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MORRISON & FOERSTER LLP

Attorneys at Law 425 Market Street San Francisco, California 94105-2482 Telephone: (415) 268-7000

Facsimile: (415) 268-7522

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U.S. Patent Application Serial No. 10/802,549 Re:

For: ADJUSTING DIE PLACEMENT ON A

SEMICONDUCTOR WAFER TO INCREASE YIELD

By: Eitan CADOURI

Our Reference: 52432-20006.00

Attached are the following:

- 1. Transmittal (1 page)
- 2. Power of Attorney (1 page)

TRANSMITTAL FORM (to be used for all correspondence effect infiliated filling) Total Number of Peges in This Submission ENCLOSURES (check all that apply) Fee Transmittal Form Fee Attached Affection After Final After Final After Final After Final After Final After Final After Form Attached Application of Time Request Information Disclaimer Examiner New Petition Petitio	TRAN (to be used for all of the used for all	NSMITTAL FORM correspondence after initial	L	Application Number Filing Date	10/802,549 March 16, 2004		
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Docket No.: 524322000600

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Eitan CADOURI

Application No.: 10/802,549

Filed: March 16, 2004

For: ADJUSTING DIE PLACEMENT ON A

SEMICONDUCTOR WAFER TO INCREASE

YIELD

Group Art Unit: 2812

Examiner: Not Yet Assigned

POWER OF ATTORNEY

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The undersigned hereby appoints the following attorneys and/or agents to prosecute this application and transact all business in the U.S. Patent and Trademark Office connected herewith:

All practitioners at Customer Number 20872.

Address all communications to:

Peter J. Yim MORRISON & FOERSTER LLP 425 Market Street San Francisco, California 94105 (415) 268-6373

For:

PDF SOLUTIONS, INC.

Dated:

7/4/24

P. Steven Melman, CFO and Vice President

Finance & Administration